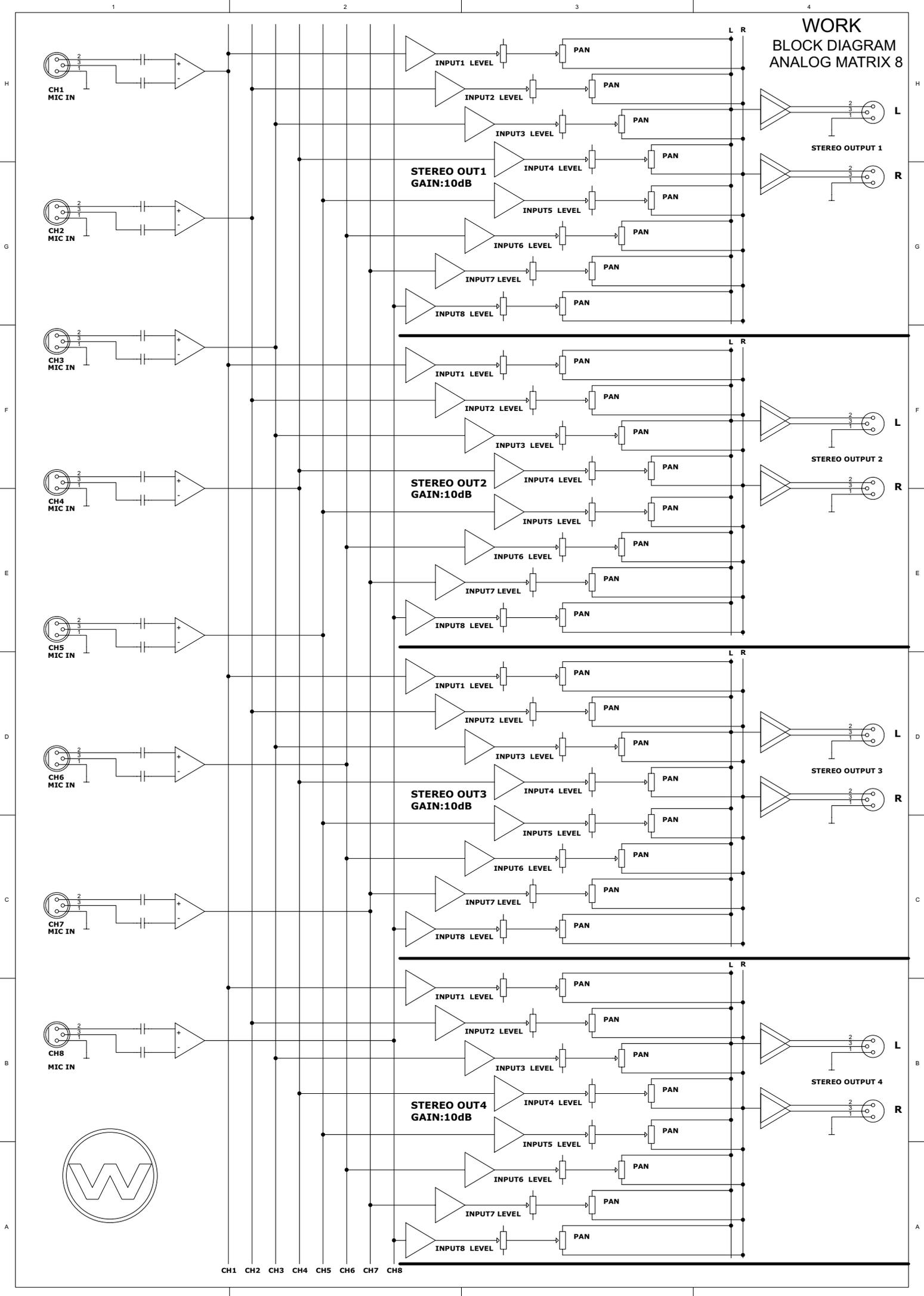


# WORK BLOCK DIAGRAM ANALOG MATRIX 8



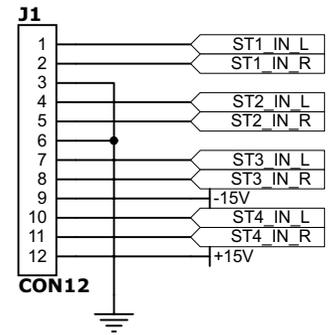
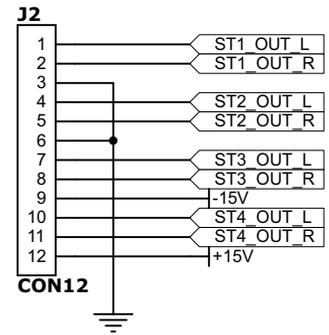
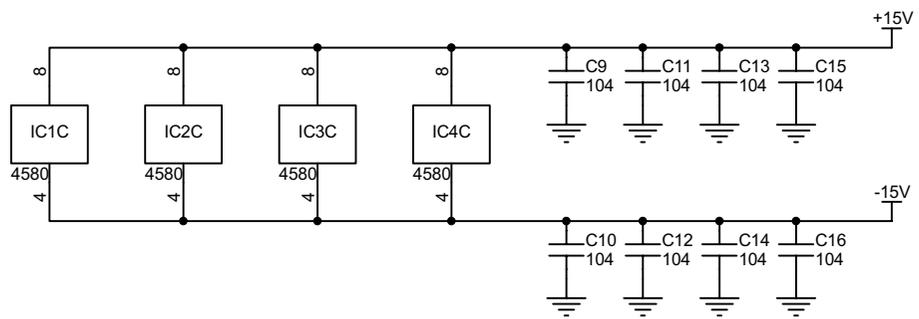
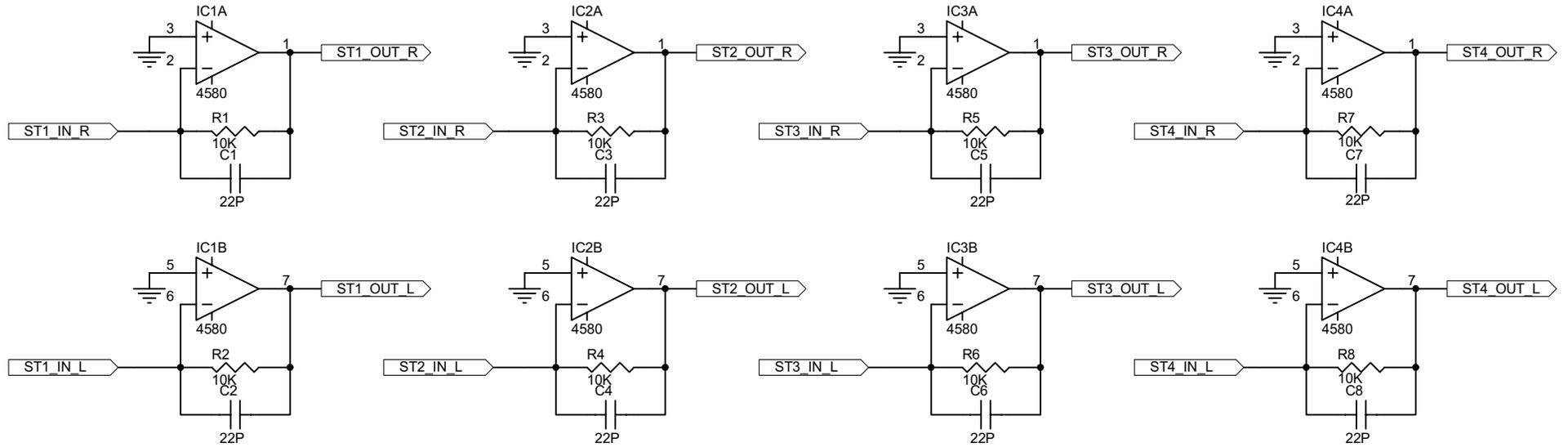
CH1 CH2 CH3 CH4 CH5 CH6 CH7 CH8

1

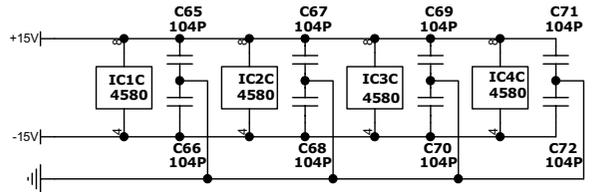
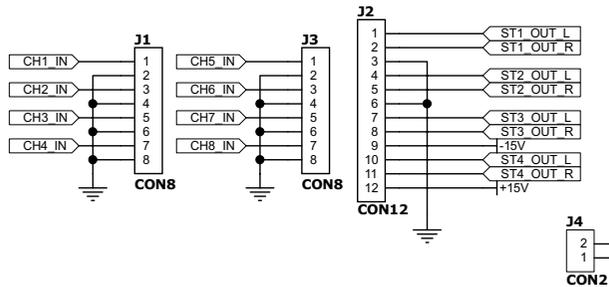
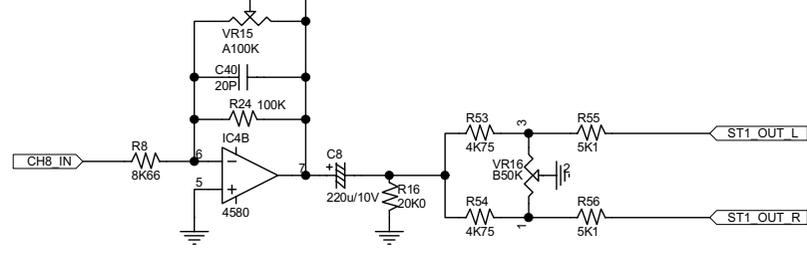
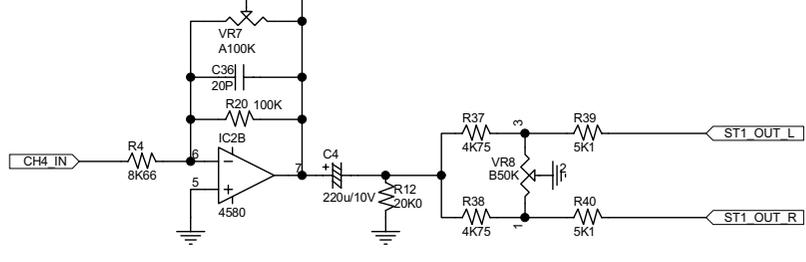
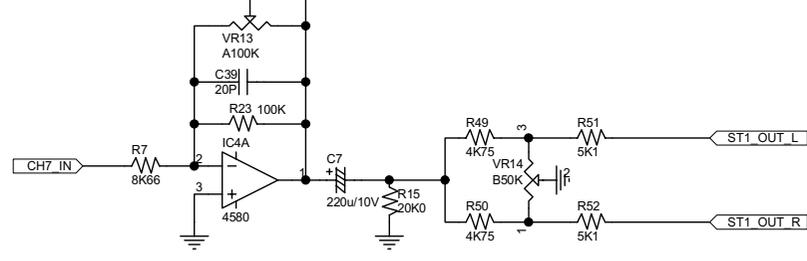
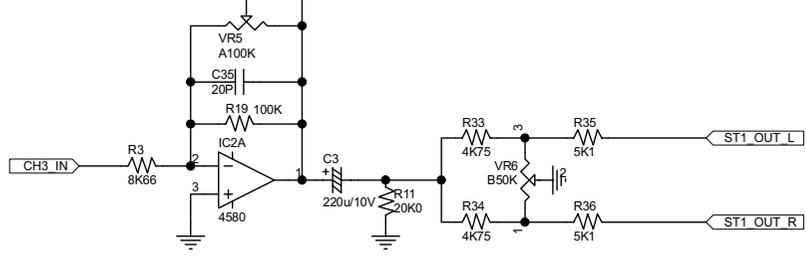
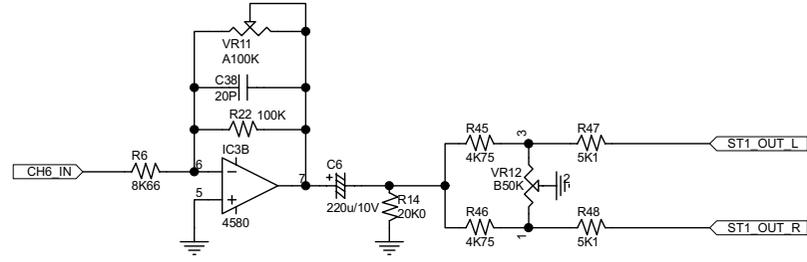
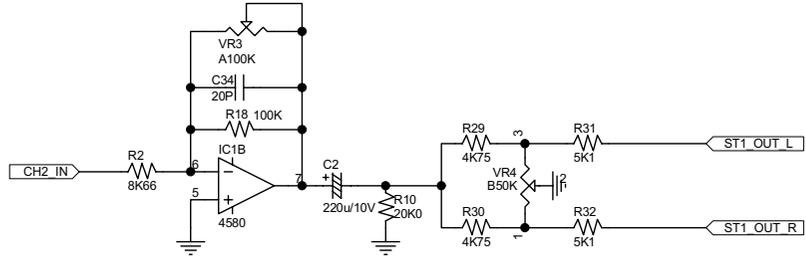
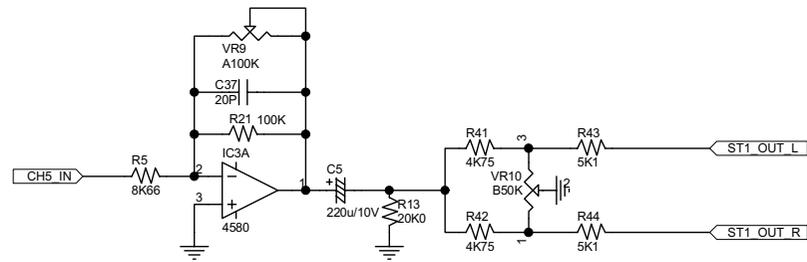
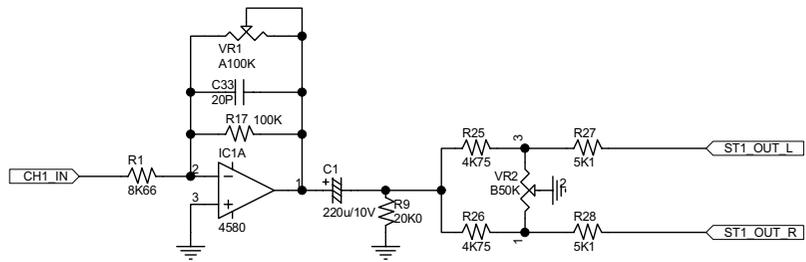
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3

4



Model	<b>ANALOGMATRIX 8</b>	<b>CONNECTOR</b>
Rev	<b>1.0</b>	
Date	<b>01/06</b>	

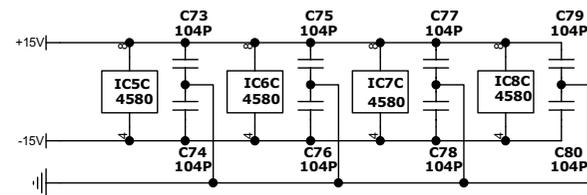
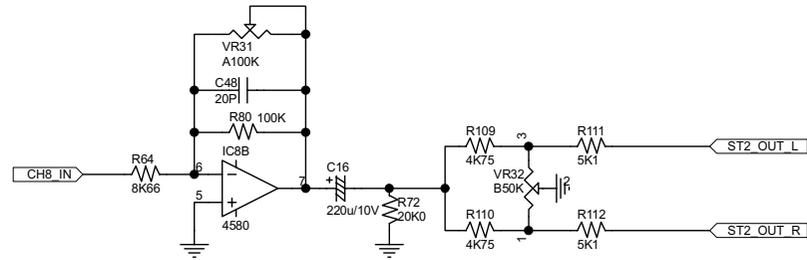
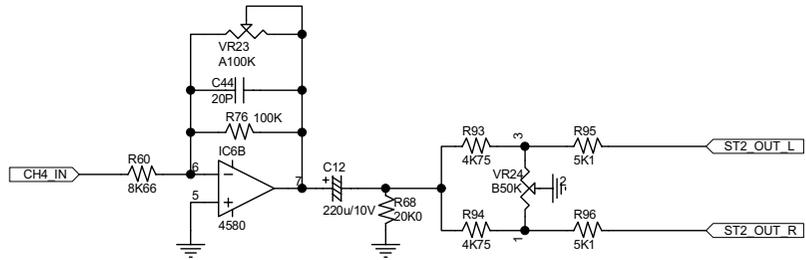
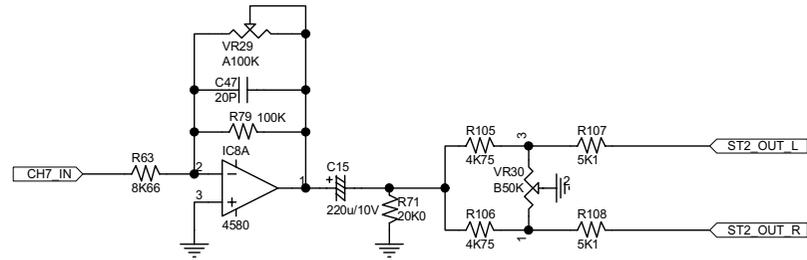
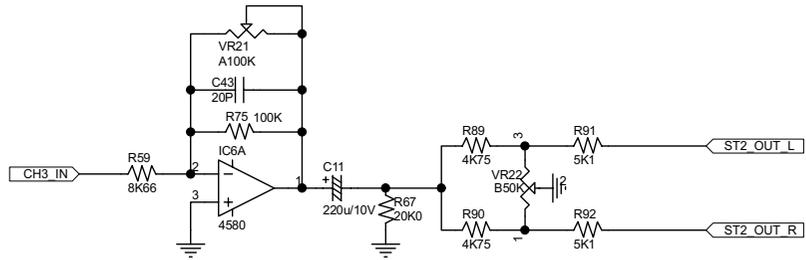
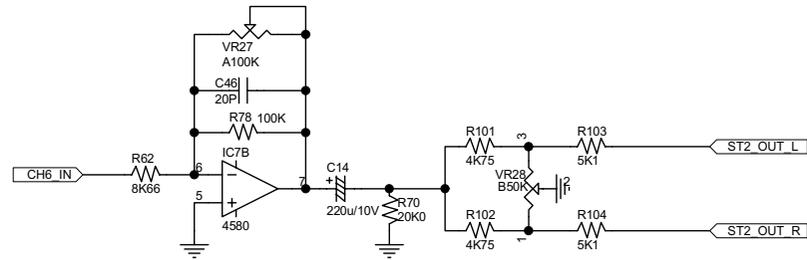
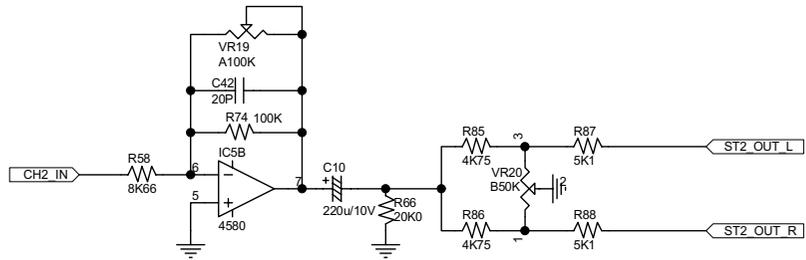
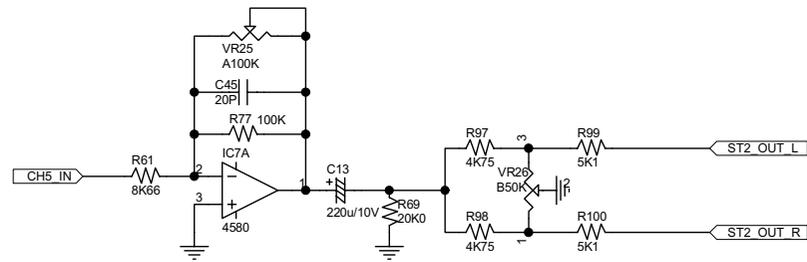
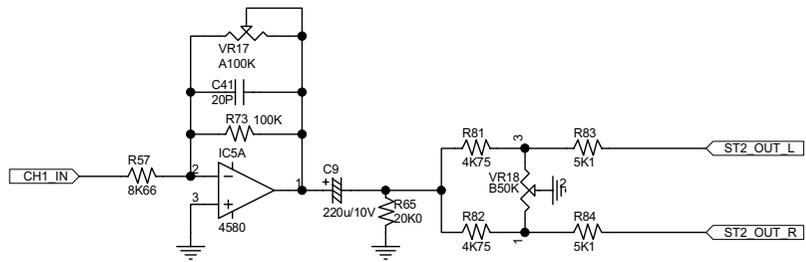


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AnalogMatrix\_STR2.Sch

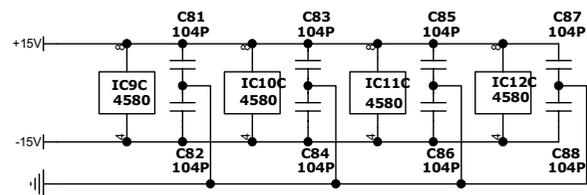
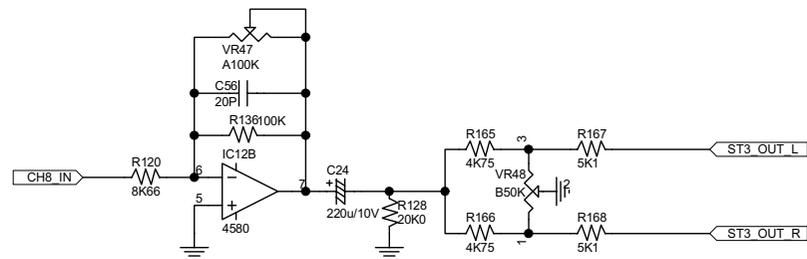
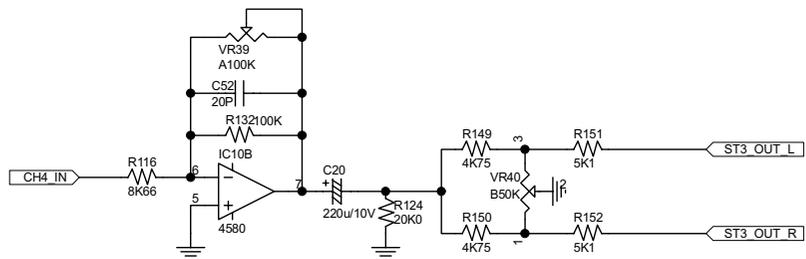
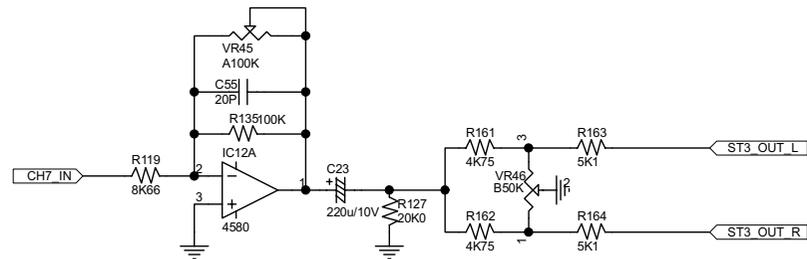
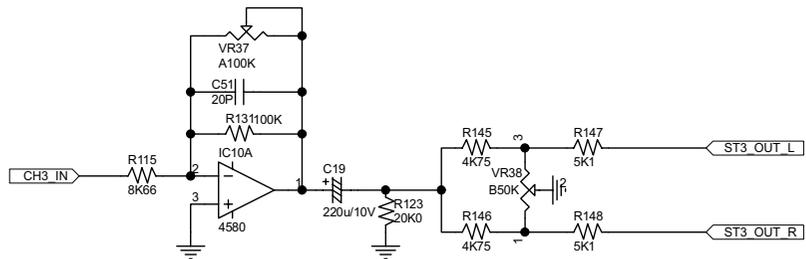
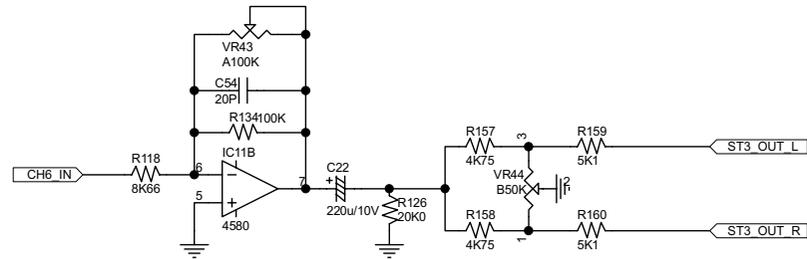
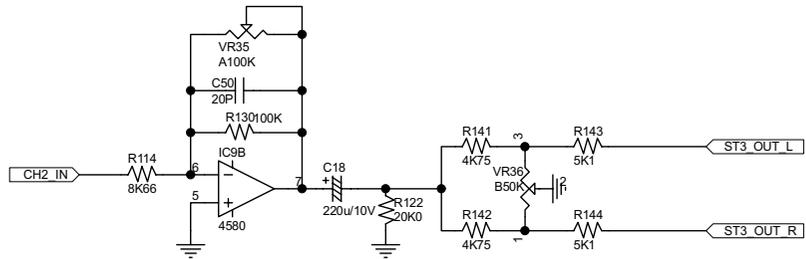
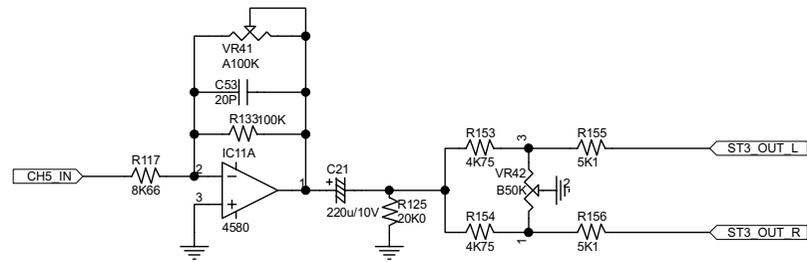
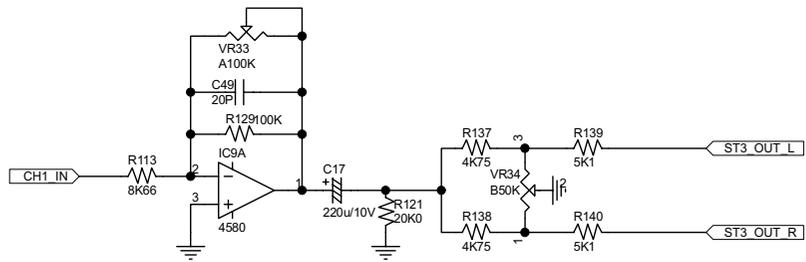
STEREO 3  
AnalogMatrix\_STR3.Sch

STEREO 4  
AnalogMatrix\_STR4.Sch

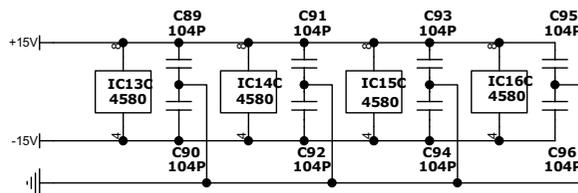
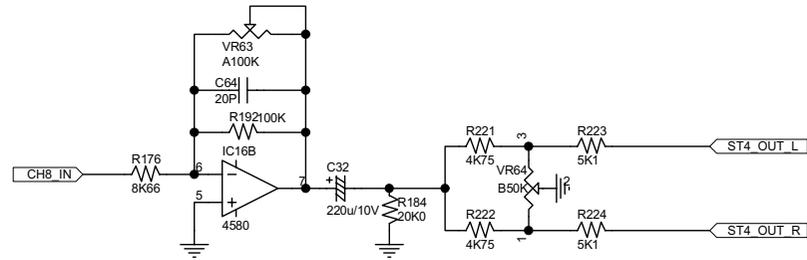
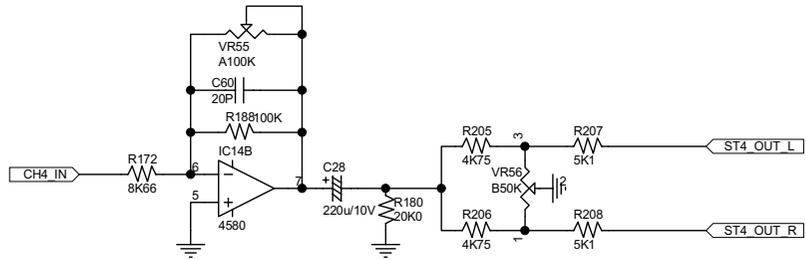
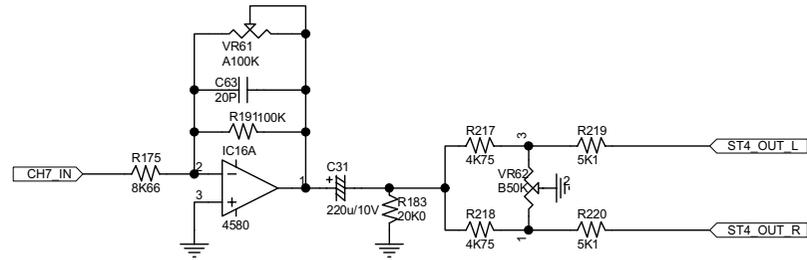
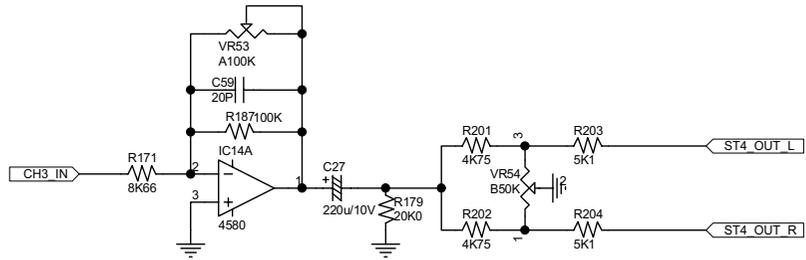
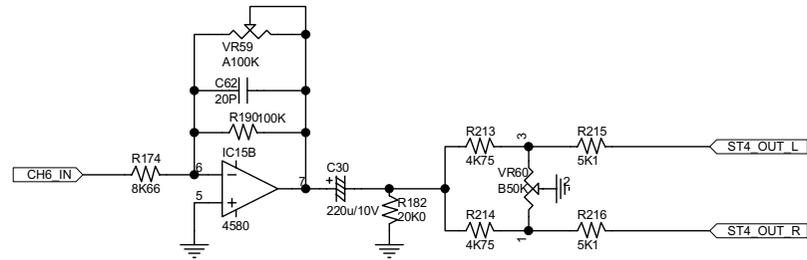
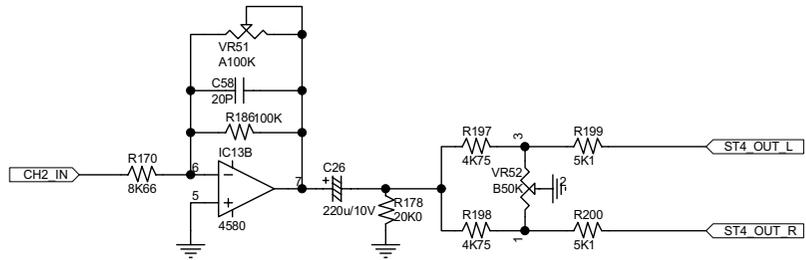
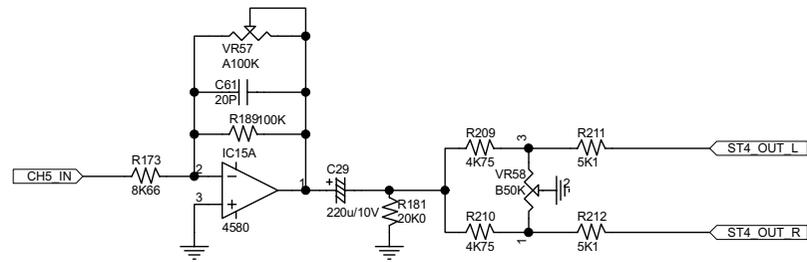
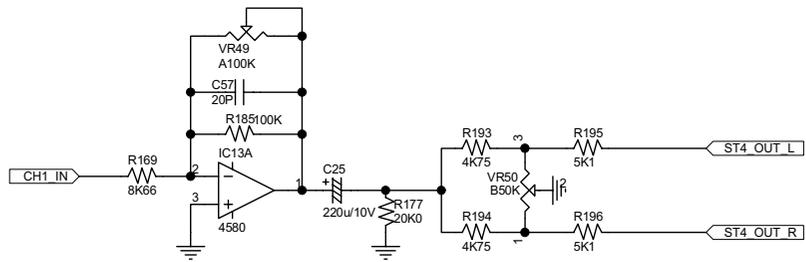
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Rev	<b>1.0</b>	
Date	<b>01/06</b>	



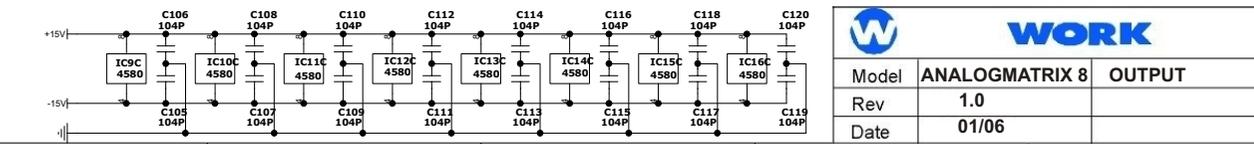
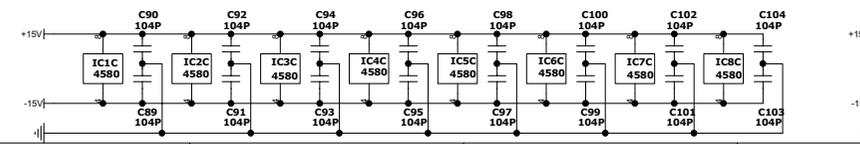
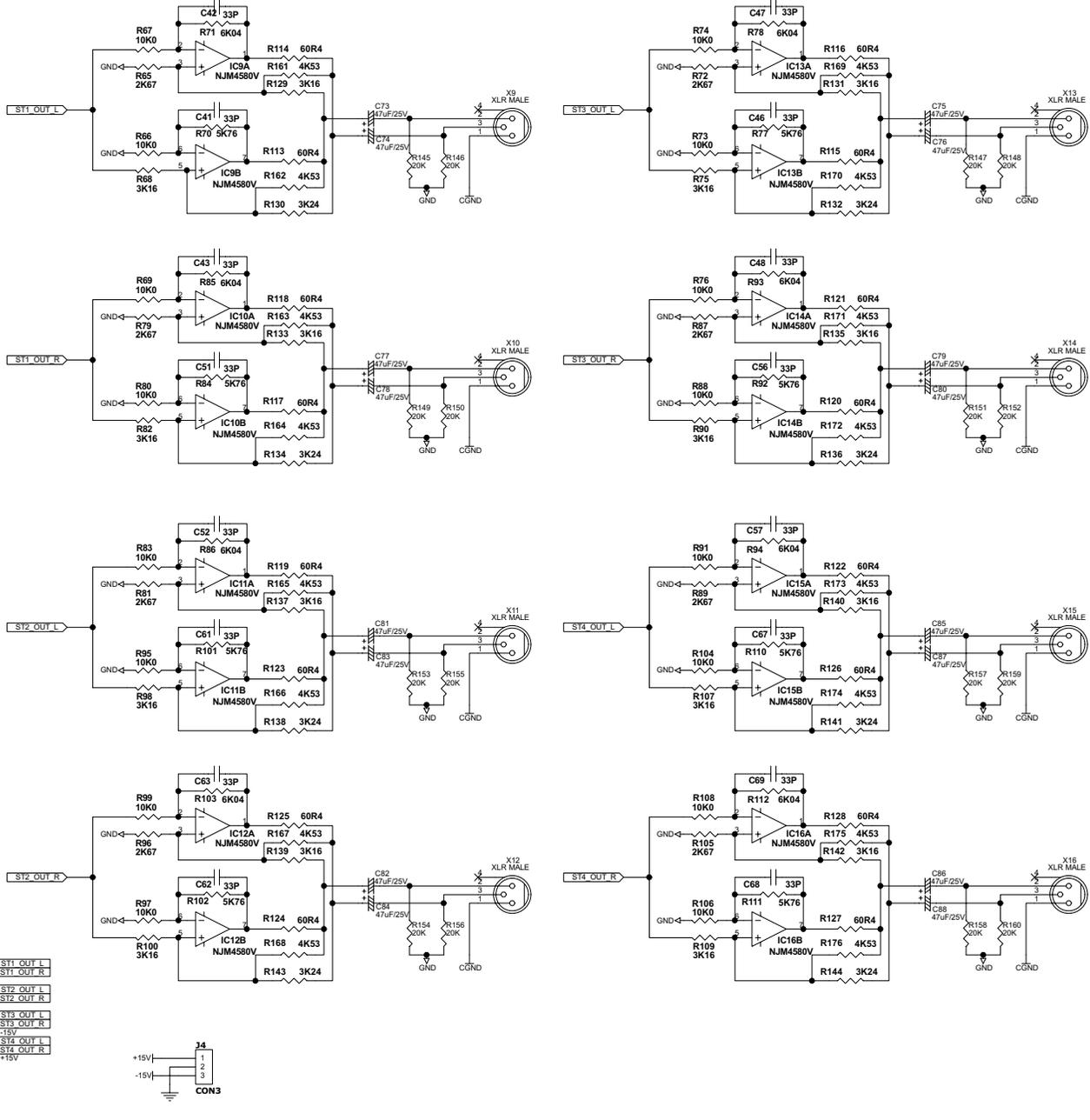
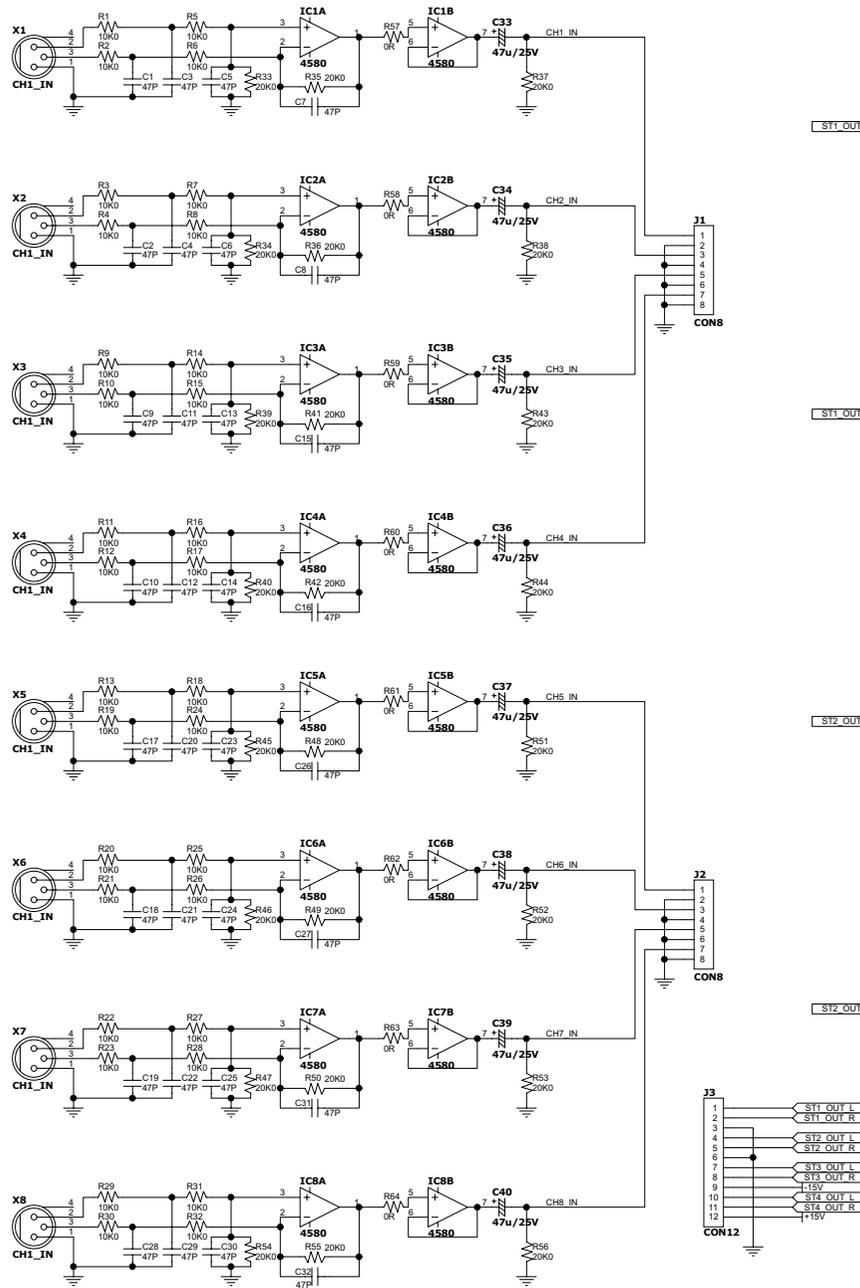
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		Rev	1.0	
		Date	01/06	



	Model	ANALOGMATRIX 8	STR3
	Rev	1.0	
	Date	01/06	



		Model	ANALOGMATRIX 8	STR4
		Rev	1.0	
Date	01/06			



		Model	ANALOGMATRIX 8	OUTPUT
		Rev	1.0	
		Date	01/06	